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paperback. Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Paperback. Publisher: Tsinghua University Pub. Date: 2010-04-14. The book is divided into eight chapters and two appendices. Chapter 1 Introduction to Logic. Chapter 2 describes the physical realization of gates and features. the first 3 to 4 describes various combinations of logic circuits and optimized to achieve. Chapter 5 describes the trigger and register. Chapter 6 introduces synchronous sequential circuits. Chapter 7 introduces asynchronous sequential circuits. with examples in Chapter 8 introduces the characteristics of digital systems and design methods. Appendix A describes EDA tool Quartus . Appendix B describes the hardware description language VHDL. Book with CD-ROM. which contains the online version of Quartus install the software. this book of pictures. tables. and VHDL source code. to facilitate student learning and teachers in producing courseware. This book is characterized by the introduction of electronic design automation (Electronic Design Automation. EDA) tools and hardware description language VHDL. so that the combination of theoretical teaching and Hands. so that learning and mastering the basic principles of design methods combined. This book can serve as institutions of higher learning computers. automation. electrical engineering and related fields. Digital Logic course materials. but also engaged in related work as engineers and technicians of the reference. Contents: Chapter 1 Introduction 1.1 switching logic circuit mathematical representation of the initial truth table 1.1.2 1.1.1 1.1.3 binary-coded form of truth table of the common analysis and synthesis 1.2 1.1.4 1.2.1 Logic Algebra Logic Algebra 1.2.2 Basic Logic Functions 1.2.3 logic operations of the basic algebraic formulas and algorithms 1.3 with AND. OR and NOT gates logic synthesis 1.4 formula method of simple logic functions o 1.5 Karnaugh map Karnaugh Map 1.5.1 1.5.2 is a graphical representation of...

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