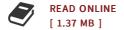


Intel Xeon Phi Coprocessor Architecture and Tools: the Guide for Application Developers

By Rezaur Rahman

APress. Paperback. Book Condition: new. BRAND NEW, Intel Xeon Phi Coprocessor Architecture and Tools: the Guide for Application Developers, Rezaur Rahman, Intel(R) Xeon Phi Coprocessor Architecture and Tools: The Guide for Application Developers provides developers a comprehensive introduction and in-depth look at the Intel Xeon Phi coprocessor architecture and the corresponding parallel data structure tools and algorithms used in the various technical computing applications for which it is suitable. It also examines the source code-level optimizations that can be performed to exploit the powerful features of the processor. Xeon Phi is at the heart of world's fastest commercial supercomputer, which thanks to the massively parallel computing capabilities of Intel Xeon Phi processors coupled with Xeon Phi coprocessors attained 33.86 teraflops of benchmark performance in 2013. Extracting such stellar performance in real-world applications requires a sophisticated understanding of the complex interaction among hardware components, Xeon Phi cores, and the applications running on them. In this book, Rezaur Rahman, an Intel leader in the development of the Xeon Phi core design that are relevant to the practice of application developers, such as its vector units, hardware...



Reviews

Without doubt, this is the very best operate by any publisher. Indeed, it can be enjoy, nevertheless an amazing and interesting literature. You may like how the writer compose this pdf.

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This book will be worth getting. Better then never, though i am quite late in start reading this one. Its been written in an extremely basic way which is only right after i finished reading this book through which actually altered me, alter the way i believe. -- **Mr. Enrico Lesch**